



भारतीय विज्ञान शिक्षा एवं अनुसंधान संस्थान मोहाली

शिक्षा मंत्रालय, भारत सरकार द्वारा स्थापित

सैक्टर 81, नॉलेज सिटी, प. ओ. मनोली, एस. ए. एस. नगर, मोहाली, पंजाब 140306

INDIAN INSTITUTE OF SCIENCE EDUCATION AND RESEARCH MOHALI

(Established by Ministry of Education, Govt. of India)

Sector-81, Knowledge city, PO-Manauli, SAS Nagar Mohali-140306, Punjab

PAN No. - AAAAI1781K GSTIN – 03AAAAI1781K2ZS

• Phone : +91-172- 2240121 • Fax : +91-172-2240124 • <http://www.iisermohali.ac.in> • Email: stores@iisermohali.ac.in

CPPP/Institute Website

IISERM (1531)21/22-Pur

Dated: 25th January 2022

ई निविदा -आमंत्रण सूचना/ NOTICE INVITING E-TENDER

Online tenders are invited on behalf of the Director, IISER Mohali in **TWO BID SYSTEM** for the **Supply & Installation of FPGA assistant pattern generator and data capture integrated circuit board, Integrated platform for evaluating the AFE5832LP IC. IC AFE5832LP should be highly-integrated analog front-end (AFE) and A high voltage source with an option to change 0 - 30kV output voltage range with a maximum current capacity of 0.5mA with an option to change the current 0 – 500 micro amperes** as per technical specification and details given below and BOQ list from the original manufacturer/supplier at CPPP i.e. <https://eprocure.gov.in/eprocure/app>. Tender documents may please be downloaded from the E-procurement portal website <https://eprocure.gov.in/eprocure/app> & Institute website www.iisermohali.ac.in.

-Sd-

सहायक कुलसचिव (भंडार और खरीद)/Assistant Registrar (P&S)



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E-TENDER NOTICE

Tender Ref.- IISERM(1531)21/22-Pur	Dated : 25 th January 2022
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Critical Date Sections

Sr.	Description	Date	Time
1.	Tender Publishing Date and time	25 th January 2022	6:00pm
2.	Tender Document download start Date & Time	25 th January 2022	6:00pm
3.	Bid Submission start Date & Time	25 th January 2022	6:00pm
4.	Bid Submission End date and Time	14 th February 2022	Upto 11:00am
5.	Tender opening Date and Time	15 th February 2022	At 11:30am

Online tenders are invited on behalf of the Director, IISER Mohali in **TWO BID SYSTEM** for following item(s) from the original manufacturer/supplier at CPPP i.e. <https://eprocure.gov.in/eprocure/app>. Tender documents may please be downloaded from the E-procurement portal website <https://eprocure.gov.in/eprocure/app> & Institute website www.iisermohali.ac.in. Tender fee in shape of Demand Draft of Rs 590/- (Non-refundable) should be submitted by bidder in favour of the Registrar, IISER Mohali payable at Mohali or through Online mode in Institute Account (Canara Bank Saving Account Number 4790101001912 and IFSC Code CNRB0004790) However, scanned copy of the Tender fee/UTR copy should be uploaded on website along with technical bid. The hard copy (if tender fee is paid) of the same in original to be sent to the address mentioned below duly superscribing the supply/work name and reference/tender ID on the envelope and same must reach before opening of the bid and if not received before due date the bid will be summarily rejected. **Micro & MSME/NSIC and Firms registered and the firms registered with concerned Ministries/ Departments, the bidders are exempted from payment of Tender Fee as per GOI notifications/GFR (2017) and Ministry of Finance OM No. F.9/4/2020-PPD dated 12 November 2020.**

Bidders will also be required to execute bond/undertaking Bid Security Declaration Form attached as ANNEXURE-I for availing exemption to submit Tender Fee.

The Original Tender Fee should be sent to (if not paid online/NEFT):

Assistant Registrar (P&S)

Indian Institute of Science Education and Research Mohali

Sector-81, Knowledge City, SAS Nagar, Mohali, Punjab,

India, Pin: 140306

Non-receipt of original Tender Fee will lead to rejection of tender.

ITEM WISE DETAILS

Sr. No.	Description	Qty. (in units)
1.	FPGA assistant pattern generator and data capture integrated circuit board features a high speed LVDS bus capable of providing 16-bits of data at 1.5 GSPS. Additionally, the board should support a dual 16-bit CMOS interface. Should be able to do high speed ADC and DAC evaluation at their full data rate with 1GB of inbuilt memory, which provides a 16-bit sample depth of 512 MB Data. Converter Module With 8 LVDS Lanes up to 1.5Gbps. should have data converter pro software (DATA CONVERTERPRO-SW) for low-voltage differential signaling (LVDS) data capture.	01
2.	Integrated platform for evaluating the AFE5832LP IC. IC AFE5832LP should be highly-integrated analog front-end (AFE) solution specifically for MHz bipolar signals where high performance and small size are required. The device integrates a complete time-gain-control (TGC) path and a continuous-wave Doppler (CWD) path. The 32-channels ce allows various power and noise combinations coding/programming with FPGA. Should contain 32-channel complete analog front end ADCs. Should have inbuilt IC for programmable-gain low-noise amplifier (LNA) Digital time-gain compensation engine with total range of 12 dB to 51 dB, 12-bit analog-to-digital converter	01
3.	A high voltage source with an option to change 0 - 30kV output voltage range with a maximum current capacity of 0.5mA with an option to change the current 0 – 500 micro amperes. Output voltage and current can be set using front panel knobs and readout from the digital panel meters. Digital Voltmeter and Current meter, Static Removal Device - Shorting Stick, Constant current / Constant Voltage mode of operation, Built-in ARC Circuit Protection. operational at 230V, 50Hz main power source. Should have the interlock shorting connector ground earth cable. HV socket set at panel. Non arcing protection with the HV case ground. Two breadboard of size each 300x500x18mm, M6 threads holes.	01

A) IMPORTANT NOTES:-

- I. **Restriction under Rule 144 (xi) of the General Financial Rules (GFRs). 2017 under sub clause 3, in terms of DOE, MoF No. F.18/37/2020-PPD dated: 8th February, 2021:**
 - (i) A bidder is permitted to procure raw material, components, sub-assemblies etc. from the vendors from countries which shares a land border with India. Such vendors will not be required to be registered with the Competent Authority, as it is not regarded as "sub-contracting".
 - (ii) However, in case a bidder has proposed to supply finished goods procured directly/indirectly form the vendors from the countries sharing land border with India, such vendor will be required to be registered with the Competent Authority.
- II. **The online updated Price BOO is in INR format. Bids are invited in INR only against domestic tender.**
- III. **The Online bids should be submitted directly by the original manufacturer/supplier. If quotation is submitted/filled by any representative/agent/dealer then they must upload a authority certificate from the principal company.**

- IV. All MSME/NSIC/Startup Units shall be considered as per provisions/rules prescribed by Govt of India.
- V. Auto-extension of last date for E-Tenders has been activated by CPP Portal which has participation 2 bids or less.

B) SUBMISSION OF TENDER

- I. All bid/ tender documents are to be uploaded online at Central Public Procurement portal i.e. <https://eprocure.gov.in/eprocure/app> only and in the designated cover/ part on the website against tender ID. Tenders/ bids shall be accepted only through online mode and no manual submission of the same shall be entertained except tender fee and Annexure I (Bid Securing Declaration Form). Late tenders will not be accepted.
- II. The online bids shall be opened at the office of the Assistant Registrar (P&S), IISER Mohali, on above given date and time. If the tender opening date happens to be on a holiday or non-working day due to any other valid reason, the tender opening process will be attended on the next working day at same time and place. IISER Mohali will not be responsible for any error like missing of schedule data while downloading by the Bidder.
- III. The bidder shall upload the tender documents duly filled in and stamped by the authorized signatory on each and every page. Tender not submitted/uploaded in the prescribed form and as per the tender terms and conditions shall be liable for rejection.
- IV. The bidder shall upload scanned copy of the PAN Card, GST number duly signed and stamped. **Also bidders applying against ‘MSME/NSIC Certificate’ issued by appropriate Authority, should ensure that the certificate attached is relevant to the area of service/supply. For example, If the tender is for “supply & installation of Desktop” the certificate should be issued for activity/area of “Computer supply and services activities etc” otherwise bid will be REJECTED without notice.**
- V. E-procurement system ensures locking on the scheduled date and time. The system will not accept any bid after the scheduled date and time of submission of bid.

C) INSTRUCTIONS

- 1. The Online bids should be submitted directly by the original manufacturer/supplier, If quotation is submitted/filled by any representative/agent/dealer then they must upload a authority certificate from the principal company for quoting the price otherwise such quotation will be rejected.
- 2. The quantity mentioned in this inquiry is and shall be deemed to be only approximate and will not in any manner be binding on the Institute. Before the deadline for submission of the online bid, IISER Mohali reserves the right to modify the tender document terms and conditions. Such amendment/modification will be notified on website against said tender ID.
- 3. The rates offered should be FOR Chandigarh/Mohali in case of firms situated outside Chandigarh/Mohali, and free delivery at the Institute premises in case of local firms. Conditional tenders will be summarily rejected.
- 4. In case of Ex-godown terms the amount of packaging forwarding freight etc. should clearly be indicated by percentage or lump sum amount. Institute has policy not to make any advance payments towards any purchase, Letter of credit can be opened if required.

5. THE INSTITUTE IS EXEMPTED FROM CUSTOM DUTY under notification no-TU/V/RG-CDE(1062)/2016 CUSTOM DT. 12.10.2018.
6. Tax: This Institute is not exempted from the payment of GST. The current rate (i.e. percentage of GST should be clearly indicated included or excluded) wherever chargeable. Please also provide/upload the copy of PAN card, GST number duly self-attested.
7. Concessional GST is applicable for all the items purchased for Research labs vide Ministry of Finance, notification no. 45/22017 dated 14.11.2017 and 47/2017 dated 14.11.2017.
8. Bidder/s quoting in currency other than **Indian Rupee (INR)** should explicitly mention the currency in which tender quoted wherever applicable in Technical Bid along the tender documents.
9. The delivery period should be specifically stated. Earlier delivery will be preferred.
10. **The firms are requested to provide/upload detailed description and specifications together with the detailed drawings, printed leaflets & literature of the article quoted** and also should enclose Technical Compliance Sheet. The name of the manufactures and country of manufacture should also invariably be stated. In the absence of these particulars and documents, the quotation is liable for rejection. Tenders not accompanied by detailed information as required, are liable to be rejected.
11. If any information furnished by the bidder is, at any stage found to be incorrect, false or fabricated, the Institute/purchaser shall have the absolute right to forfeit E.M.D. and security deposits, in addition to cancellation of contract, forfeiting the warranty/performance Guarantees and other action in accordance with law, such as black-listing, risk & cost etc.
12. Validity of offer: 90 days.
13. The warranty period after satisfactory installation should be mentioned and firm should replace all manufacturing defect parts/ whole item under warranty without any extra cost including clearance, freight, taxes. Security deposit/Performance Bank Guarantee @ 3% of the value of supply order, in terms of *Ministry of Finance, DoE, GOI, OM. No. F.9/42020-PPD dated 12-11-2020 and extension of vide circular no. F.9/4/2020-PPD dated: 30th December 2021* as per norms may be sought from the firms.
14. The right to reject all or any of the quotation and to split up the requirements for itemized L-1 or relax any or all the above conditions without assigning any reason is reserved by the IISER Mohali. For any corrigendum and addendum please be checked the website <https://eprocure.gov.in/eprocure/app> and <http://www.iisermohali.ac.in>
15. Disputes, if any, shall be subject to jurisdiction in the court of Mohali only.

–Sd-
Assistant Registrar (P&S)

ANNEXURE-I

Bid Securing Declaration Form

Date:_____ E-Tender No. _____ E-Tender ID _____

To (insert complete name and address of the purchaser)

I/We. The undersigned, declare that:

I/We understand that, according to your conditions, bids must be supported by a Bid Securing Declaration.

I/We accept that I/We may be disqualified from bidding for any contract with you for a period of one year from the date of notification if I am /We are in a breach of any obligation under the bid conditions, because I/We

a) have withdrawn/modified/amended, impairs or derogates from the tender, my/our Bid during the period of bid validity specified in the form of Bid; or

b) having been notified of the acceptance of our Bid by the purchaser during the period of bid validity (i) fail or reuse to execute the contract, if required, or (ii) fail or refuse to furnish the Performance Security, in accordance with the Instructions to Bidders. I/We understand this Bid Securing Declaration shall cease to be valid if I am/we are not the successful Bidder, upon the earlier of (i) the receipt of your notification of the name of the successful Bidder; or (ii) thirty days after the expiration of the validity of my/our Bid.

Signed: (insert signature of person whose name and capacity are shown)
in the capacity of (insert legal capacity of person signing the Bid Securing Declaration)

Name: (insert complete name of person signing the Bid Securing Declaration)
Duly authorized to sign the bid for an on behalf of (insert complete name of Bidder)

Dated on _____ day of _____ (insert date of signing)
Corporate Seal (where appropriate)

(Note: In case of a Joint Venture, the Bid Securing Declaration must be in the name of all partners to the Joint Venture that submits the bid)

PS: *Furnish the above in original stationary/letter head with signed and sealed.*